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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/578,052	07/12/2006	Francesco Maone	DE04 0007 US1	8894
65913	7550	04/24/2008		
NXP, B.V. NXP INTELLECTUAL PROPERTY DEPARTMENT M/S41-SJ 1109 MCKAY DRIVE SAN JOSE, CA 95131				
EXAMINER				
TRA, ANH QUAN				
ART UNIT		PAPER NUMBER		
2816				
NOTIFICATION DATE		DELIVERY MODE		
04/24/2008		ELECTRONIC		

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

ip.department.us@nxp.com

Office Action Summary

Application No.

10/578,052

Applicant(s)

MAONE, FRANCESCO

Examiner

QUAN TRA

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 12 July 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-11 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1,2 and 7-11 is/are rejected.
- 7) ☒ Claim(s) 3-6 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO/CDC)
- 4) ☐ Interview Summary (PTO-413)
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____
- Paper No(s)/Mail Date _____

DETAILED ACTION

Claim Rejections - 35 USC § 112

1. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

2. Claim 11 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 11 is misdescriptive, thereby renders the claim indefinite. It is misdescriptive to recite " the switching transistor is provided to be switched on, when the supply voltage falls under a certain reference voltage". Page 10, lines 14-16, of the specification teaches that transistor 18 is off when Vdd is lower than the threshold voltage.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1, 2 and 7 are rejected under 35 U.S.C. 102(b) as being anticipated by Dessard et al. (USP 6714079).

As to claim 1, Dessard et al.'s figure 3 shows a comparator, comprising: a differential amplifier having differential inputs (VIN+, VIN-) forming the comparator inputs, and a first and second amplifier output (nodes between (M1 and M5) and between (M2 and M6) forming the comparator outputs of a first comparator stage, a first differential current amplifier (M5, M9)

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connected with its inputs (gates of M5 and M9) to the amplifier outputs and connected with its output (sources of M5 and M9) to the first amplifier output and a second differential current amplifier (M10, M6) connected with its inputs (gates) to the amplifier outputs and connected with its output to the second amplifier output.

As to claim 2, figure 3 shows a second comparator stage (M7, M3, M8, M4) having an output amplifier whose inputs (gates of M7 and M8) are connected to the comparator outputs of the first comparator stage and whose output forms the comparator output of said second comparator stage.

As to claim 7, figure 3 shows that the differential amplifier comprises a first and a second input transistor (M1, M2) whose control outputs are connected to an auxiliary current source (4).

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 8-11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Dessard et al. (USP 6714079) in view of Yamaguchi (USP 6400215).

As to claim 8, Dessard et al.'s figure 3 fails to show the claimed detail of the current source. However, Yamaguchi's figure 6 shows a constant current source. Therefore, it would have been obvious to one having ordinary skill in the art to use Yamaguchi's current source for Dessard et al.'s current source for the purpose of providing a precise constant current. Thus, the modified Dessard et al.'s figure 3 shows that the auxiliary current source (Yamaguchi's 11') is connected via a current mirror (Tr11, Tr2) to the input transistors.

As to claim 9, the modified Dessard et al.'s figure 3 shows that the current mirror is connected via a further current mirror (Tr31, Tr33) to the input transistors.

As to claim 10, the modified Dessard et al.'s figure 3 shows the auxiliary current source (Yamagushi's 11') is connectable via a switching transistor (Tr22) to the current mirror.

Insofar as understood to claim 11, the modified Dessard et al.'s figure 3 shows the switching transistor is provided to be switched on (or off), when the supply voltage falls under a certain reference voltage (maybe any value).

Allowable Subject Matter

6. Claims 3-6 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to QUAN TRA whose telephone number is (571)272-1755. The examiner can normally be reached on 8:00 AM - 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Drew N. Richards can be reached on (571) 272-1736. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/QUAN TRA/
Primary Examiner
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